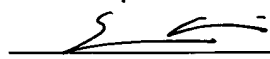


23. (New) The semiconductor device according to claim 14 or 15, wherein the semiconductor device includes one or a plurality of a clock generating circuit, a data demodulation/modulation circuit, and an interface circuit.

REMARKS

New claims 14-23 have been added. Examination on the merits is requested.

Respectfully submitted,



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